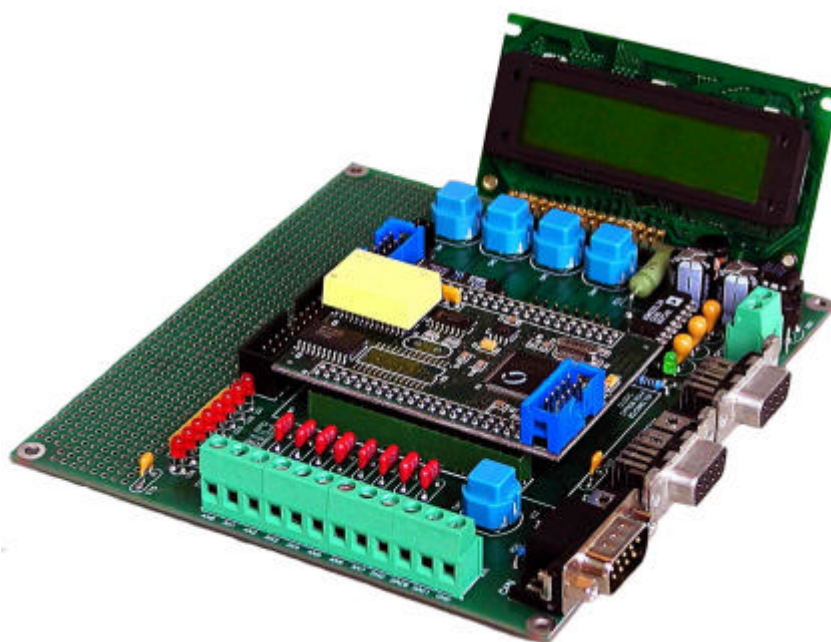


EVBCYF

DATA SHEET



EVBCYF

Evaluation board for CYF020 and CYF120 modules

Product specification

January, 2004

EVBCYF Leaflet

Version 1.1

DESCRIPTION

EVBCYF is the evaluation board for CYF020 or CYF120 modules. It allows quick start prototyping and testing your software applications for CYF020 or CYF120 modules. It's also great for educational purposes, etc. Free prototyping area allows building of your own hardware add-ons. CAN expansion connector allows connecting several EVBCYFs into the small LAN, which is also possible by means of the built-in buffered SMS-bus interface.

Comes with CD packed with application notes describing how to use on-board features, including sample software. Also, a library of software drivers for on-board peripherals is available.

Together with CYF020's powerful JTAG debug and programming capabilities, it makes excellent choice for both beginners and experienced users.

FEATURES

- Compact t 5,5"x5,0" (141mmx128mm) size, double-sided PCB
- Socket for CYF020 or CYF120 module
- Two RS-232 serial channels with DB-9 sockets
- One CAN (CiA pinout) DB9 plug
- 8 analog inputs 0..10V range
- 2 analog outputs 0..2,4V range
- 2x16 characters LCD module with LED backlight
- on-board serial (I²C) 16KB EEROM of 24C16 type
- 4 user-defined function push-buttons
- 8 red LED's , user controllable
- 10-pin serial expansion connector featuring SPI and buffered SMS-bus (I2C compatible)
- 26-pin expansion connector
- 5,3"x1,0" prototyping area
- on-board switching regulator accepting wide range of AC/DC input voltages (7..28V)

TOP VIEW

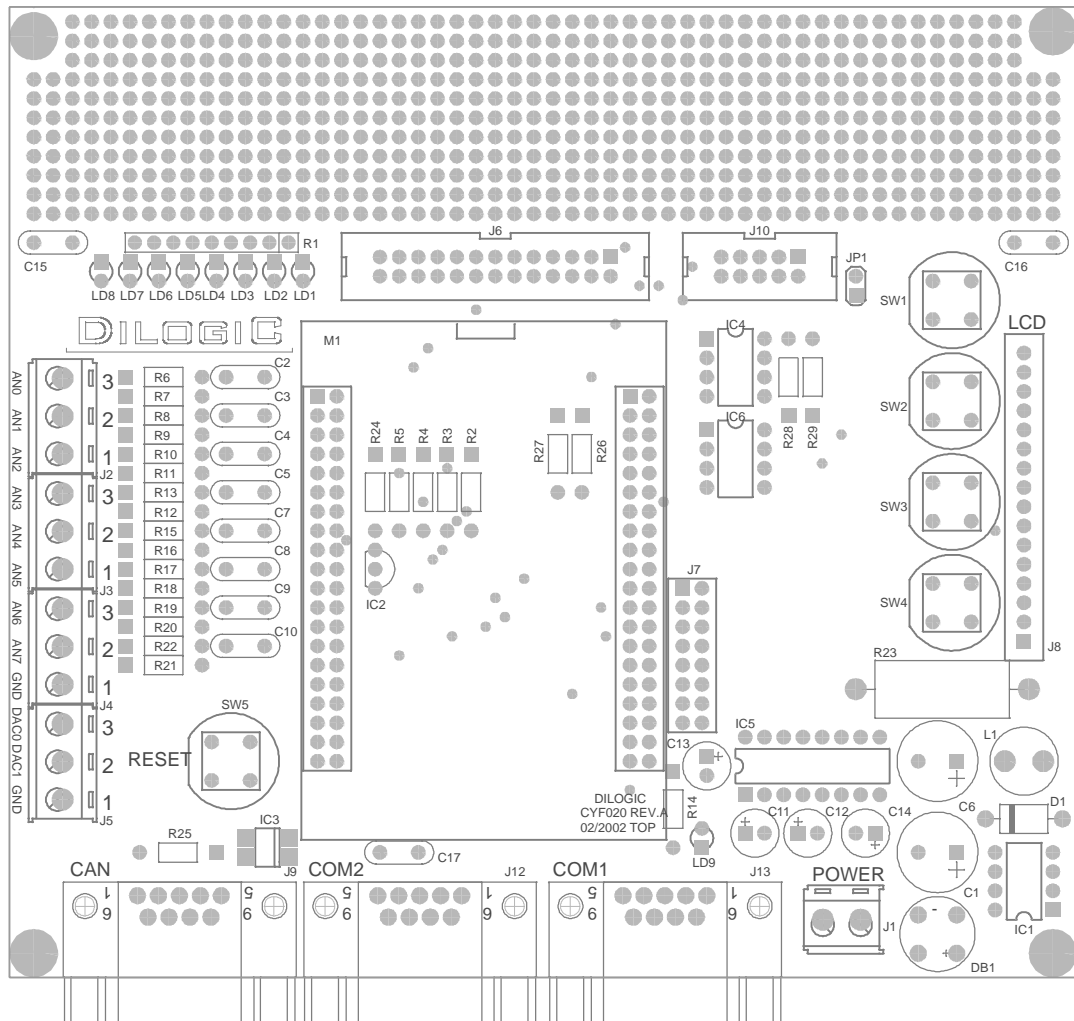


Fig.3 EVBCYF top view

CONNECTOR PINOUT

Digital signals

Connector J10

Pin	Description
1.	SDA
2.	VCC ⁽¹⁾
3.	SCL
4.	SCK
5.	P26
6.	MISO
7.	P27
8.	MOSI
9.	GND
10.	NSS

NOTE:

1. Pin2 can be connected to VCC via jumper J11 !

Connectors J12 and J13

Pin	Description
1.	n.c.
2.	RxD
3.	TxD
4.	n.c.
5.	GND
6.	n.c.
7.	n.c.
8.	n.c.
9.	n.c.

Connector J9

Pin	Description
1.	n.c.
2.	CAN-L
3.	GND
4.	n.c.
5.	n.c.
6.	n.c.
7.	CAN-H
8.	n.c.
9.	n.c.

Connector J6

Pin	Description
1.	CP0+
2.	CP1+
3.	CP0-
4.	CP1-
5.	P12
6.	P13
7.	P14
8.	P15
9.	P16
10.	P17
11.	P20
12.	P21
13.	P22
14.	P23
15.	P24
16.	P25
17.	P26
18.	P27
19.	P30
20.	P31
21.	P32
22.	P33
23.	P34
24.	P35
25.	P36
26.	P37

Connector J8

Pin	Description
1.	Backlight +V
2.	GND
3.	GND
4.	VCC
5.	GND
6.	RS
7.	R/W
8.	E
9.	n.c.
10.	n.c.
11.	n.c.
12.	n.c.
13.	DB4
14.	DB5
15.	DB6
16.	DB7

CONNECTOR J7

Connector J7 is used for routing of CAN controller interrupt signal. As C8051F020 and C8051F120 processors do not have fixed pinout (port signals can be re-routed through crossbar), it is necessary to connect CAN controller interrupt signal externally to the appropriate INT input. User can select between 8 port inputs, to better suit specific application:

Pin pair	Description
1-2	P2.0
3-4	P2.1
5-6	P1.2
7-8	P1.3
9-10	P1.4
11-12	P1.5
13-14	P1.6
15-16	P1.7

ANALOG INPUTS / OUTPUTS

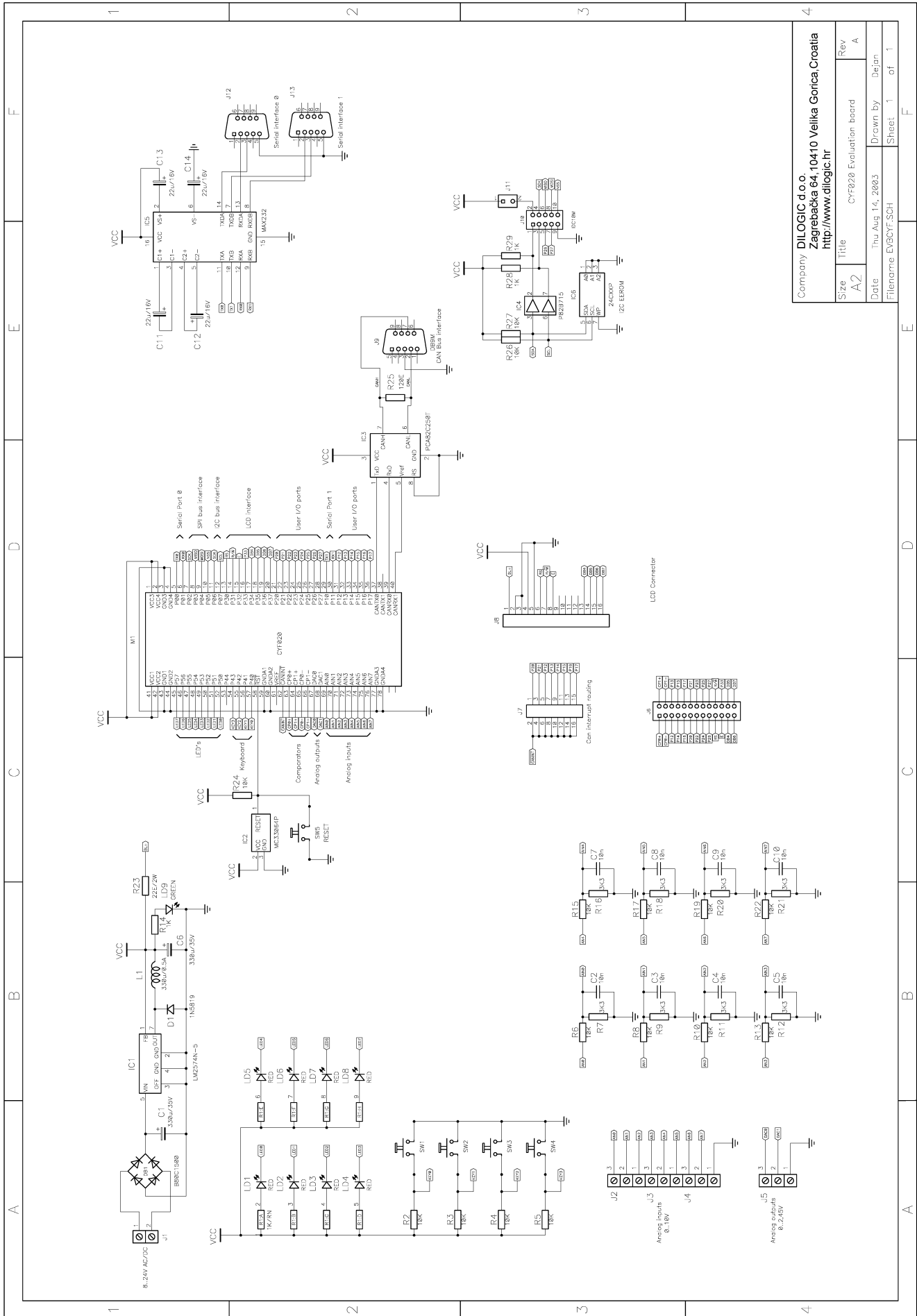
Analog inputs are connected via screw terminals J2, J3 and J4:

Pin	Description
J2-3	AN0
J2-2	AN1
J2-1	AN2
J3-3	AN3
J3-2	AN4
J3-1	AN5
J4-3	AN6
J4-2	AN7
J4-1	GND

Each input has a divider that brings input voltage from 0..10V into the 0..2,5V range.

Two analog outputs (0..2,45V) are routed to the J5 connector:

Pin	Description
J5-3	DAC0
J5-2	DAC1
J5-1	GND



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